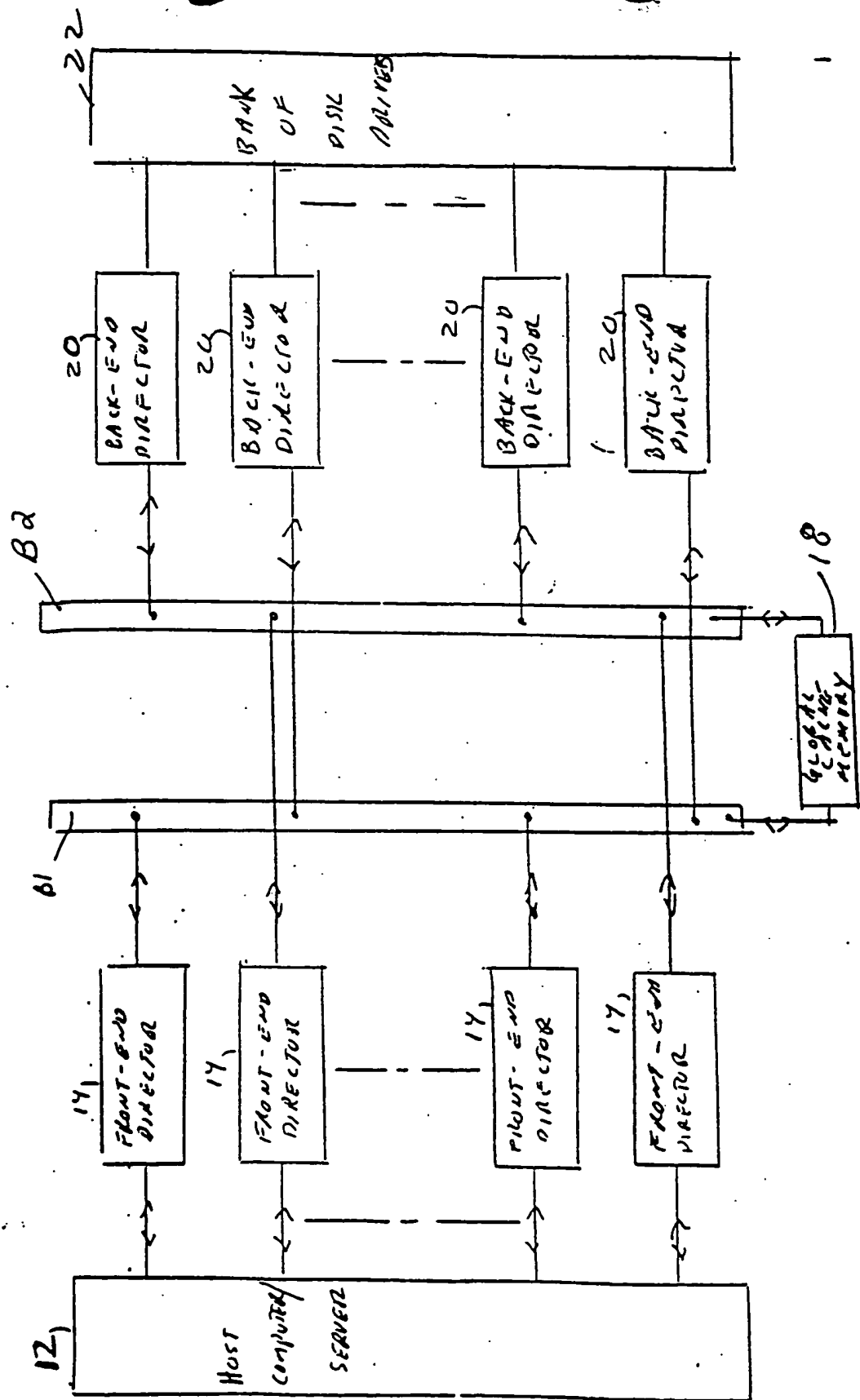


005230" 5050860

FIG. 1
Broad Net



1001

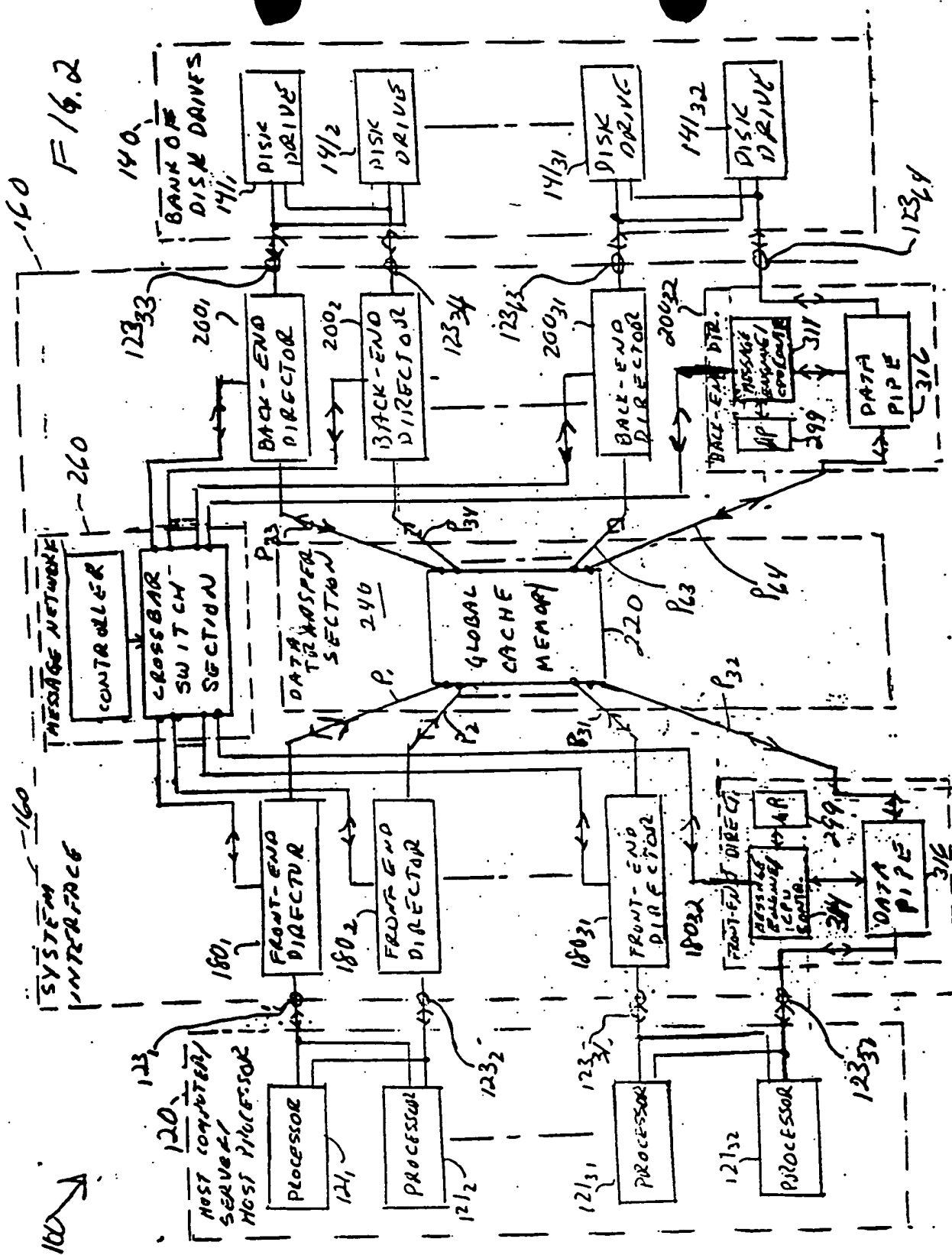


FIG. 3

DIRECTOR BOARD

CACHE MEMORY 220

FIG. 4

BACKPLANE 302

TO/FROM DISK DRIVES 140

TO/FROM HOST COMPUTER/ SERVER/ HOST PROCESSOR 120

MESSAGE NETWORK BOARD 304

MESSAGE NETWORK BOARD 3042

DIRECTOR BOARD

DIRECTOR BOARD

DIRECTOR BOARD

DIRECTOR BOARD

DIRECTOR BOARD

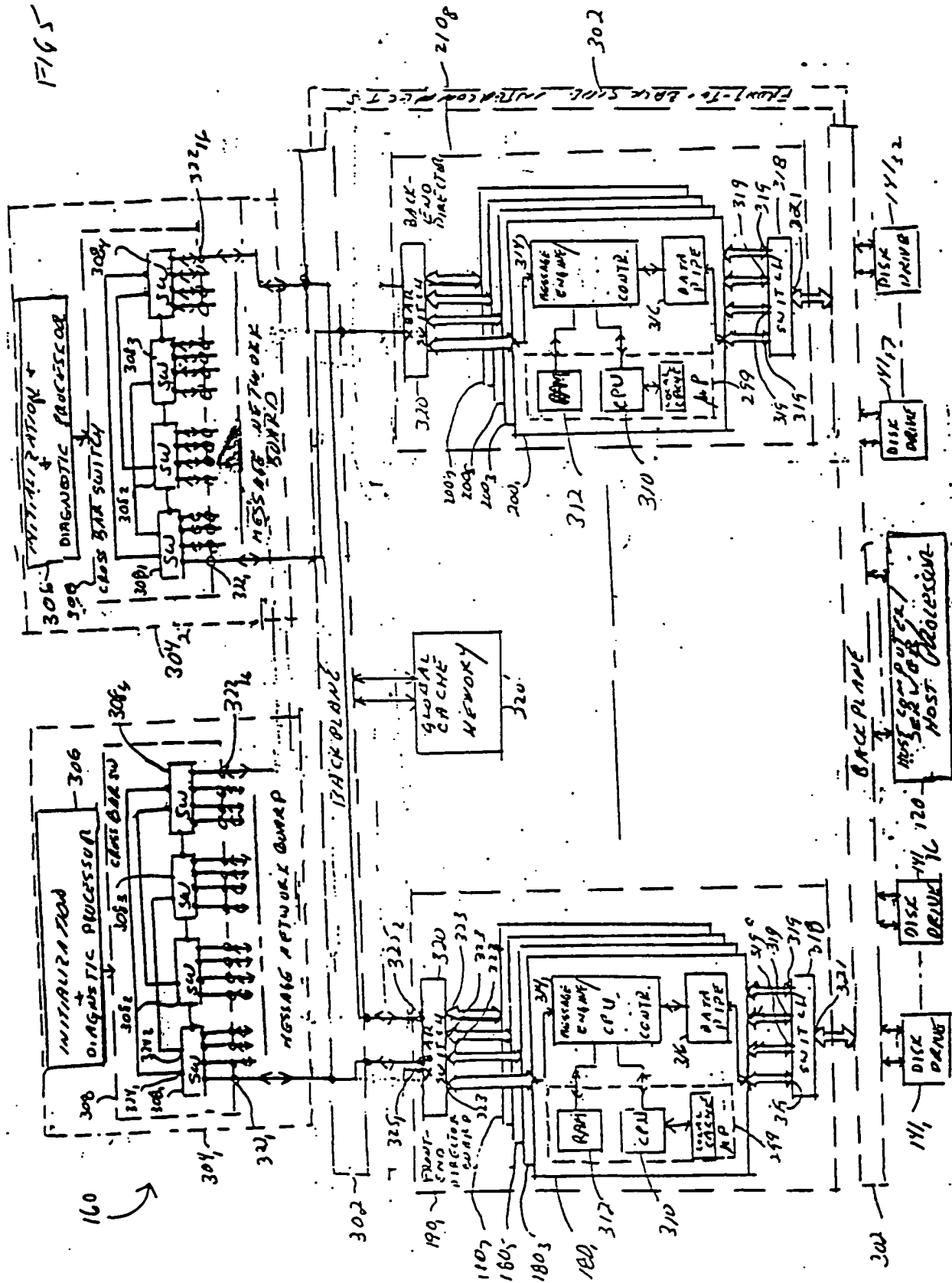
DIRECTOR BOARD

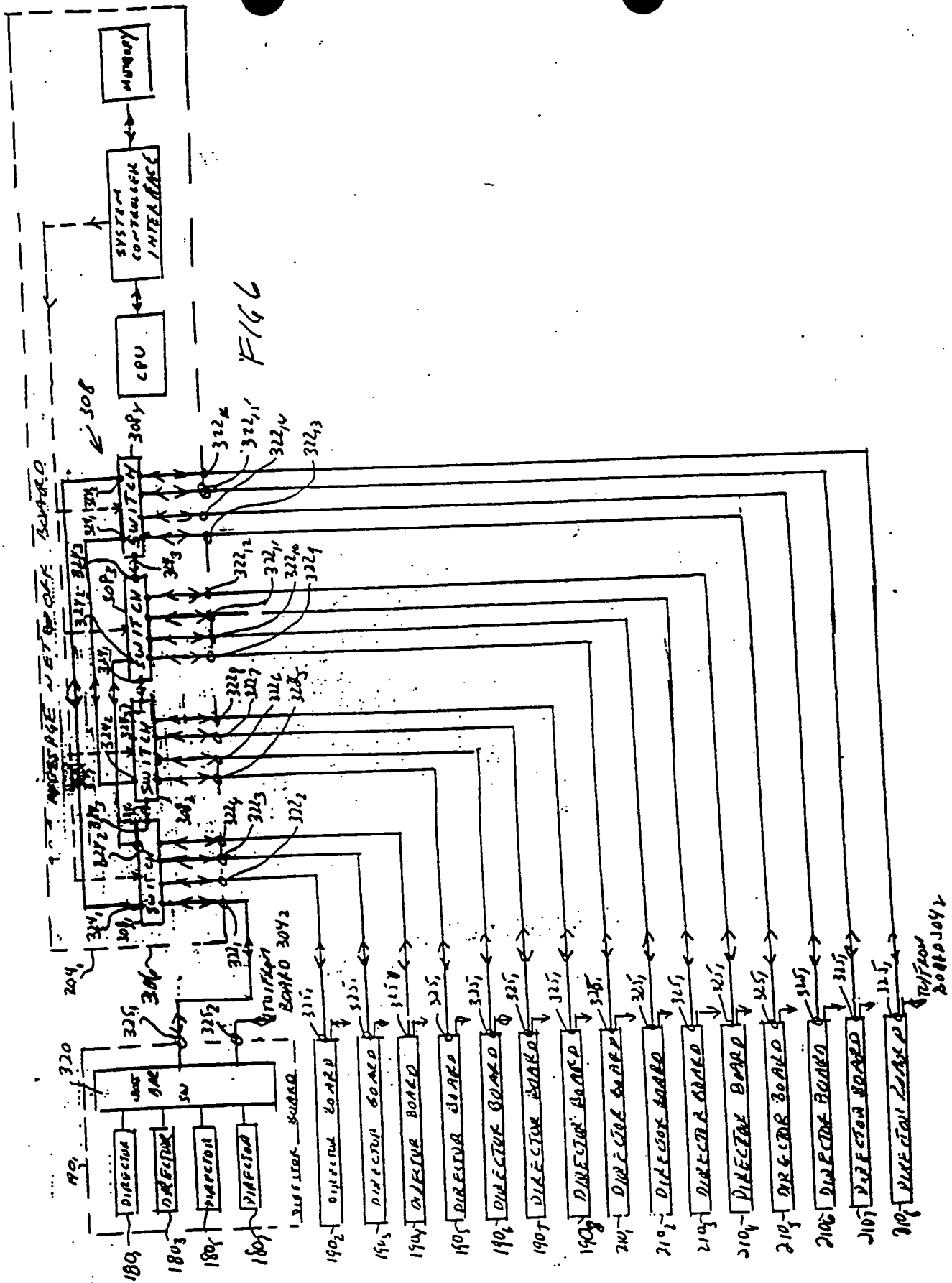
BACKPLANE 302

CACHE MEMORY BOARD

CACHE MEMORY BOARD

1565

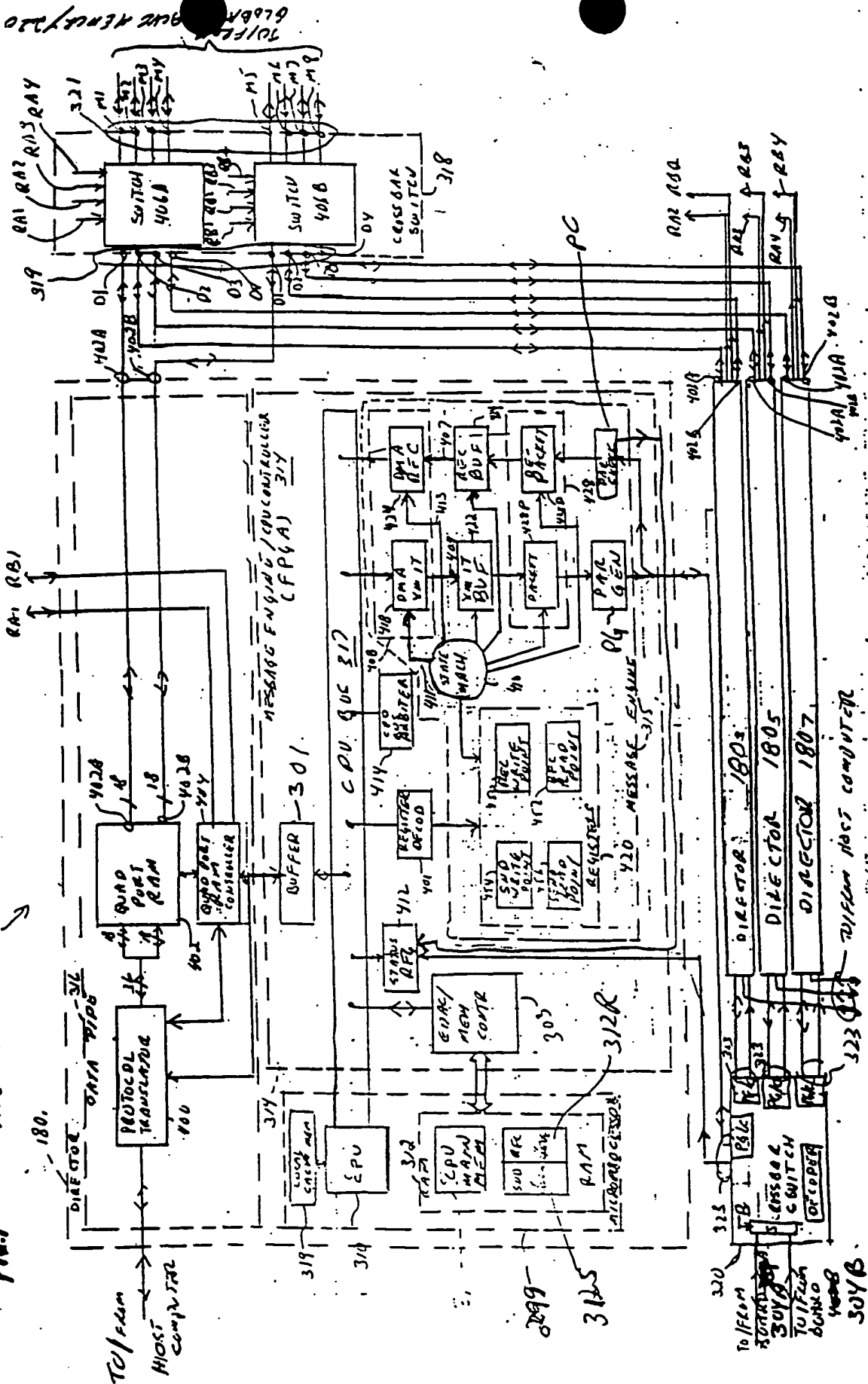




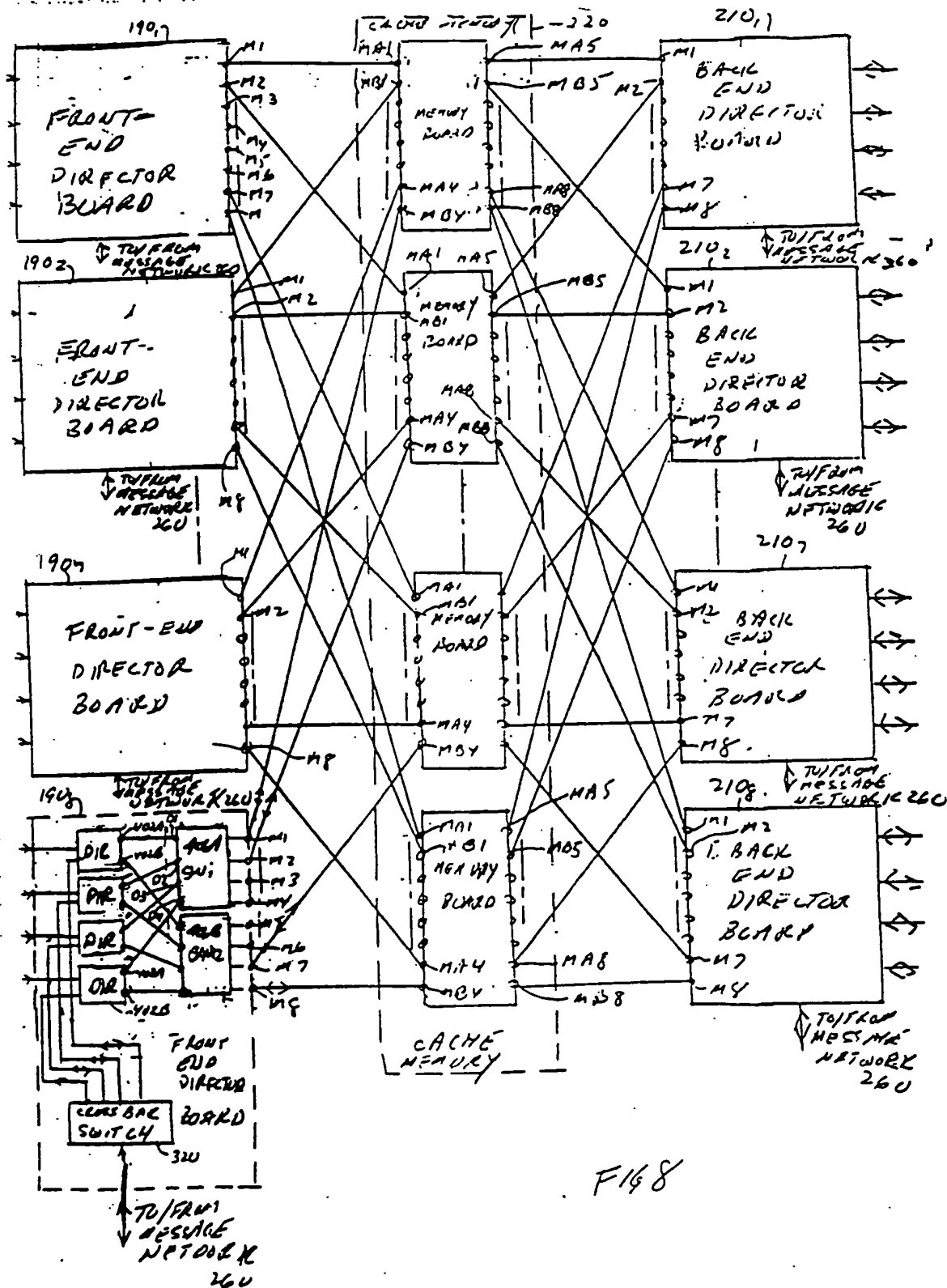
005230" 50590500

FIG 7

DIRECTOR BOARD 1801



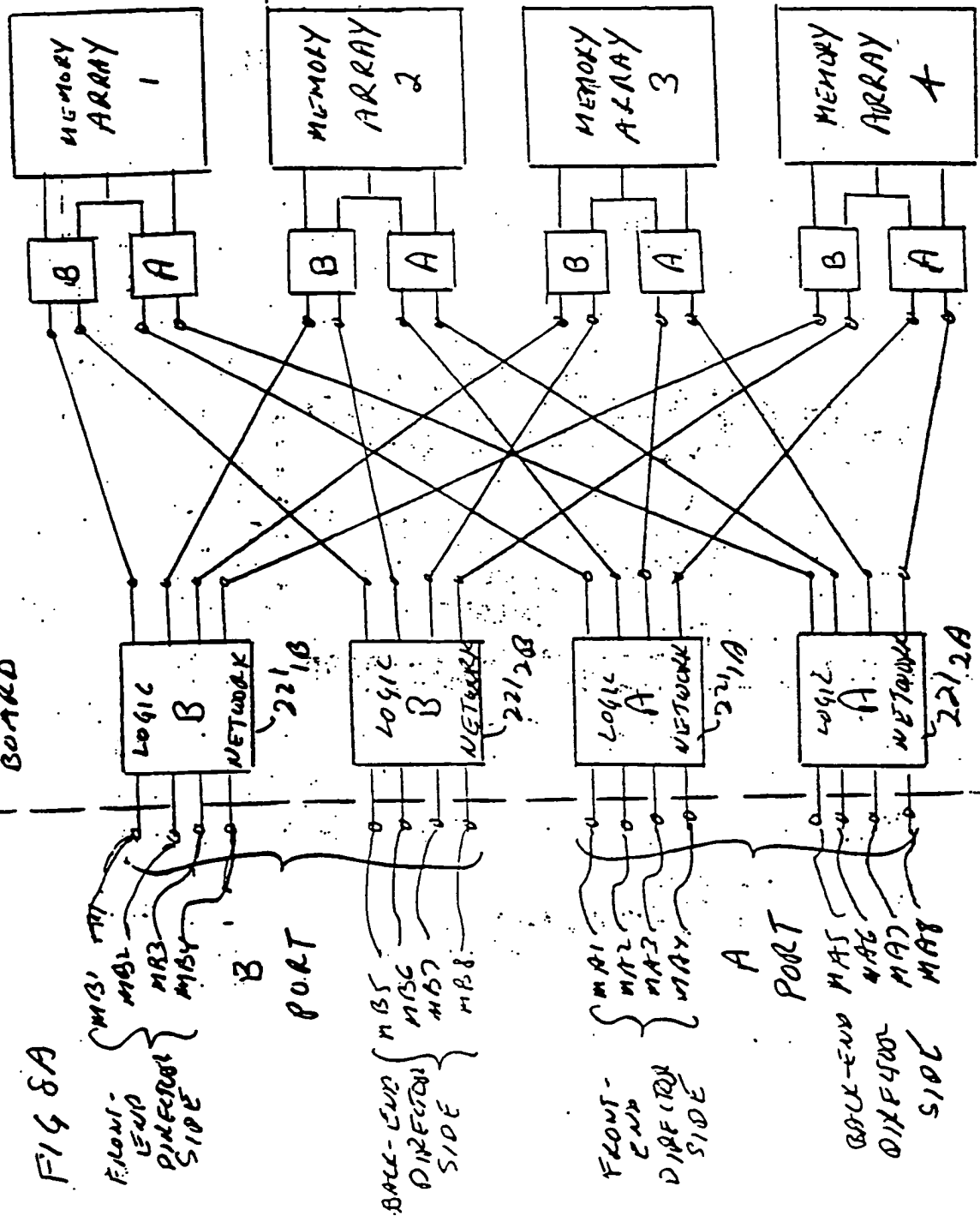
THE



F168

005250 52010960

FIG 8A



THE UNIVERSITY OF CHICAGO

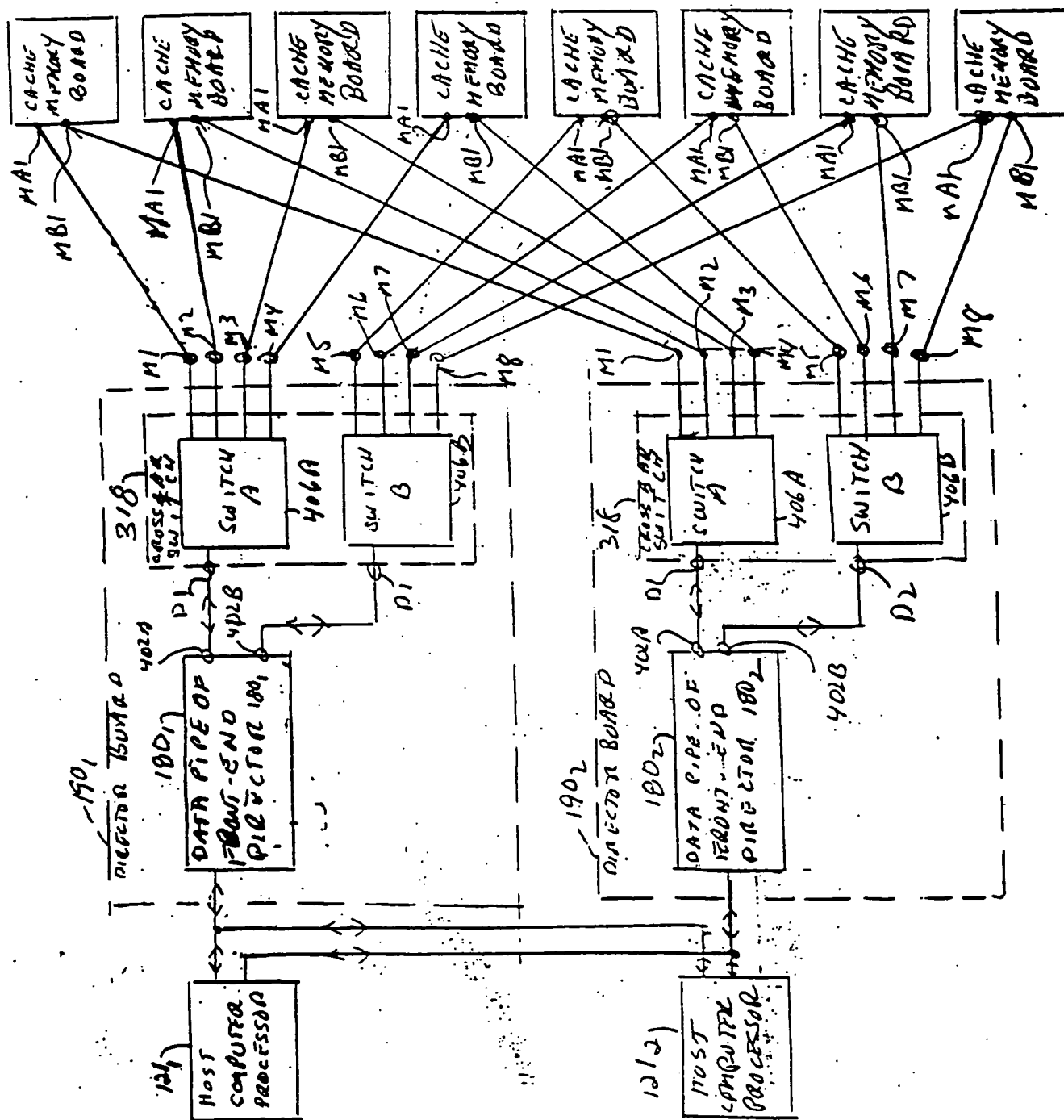
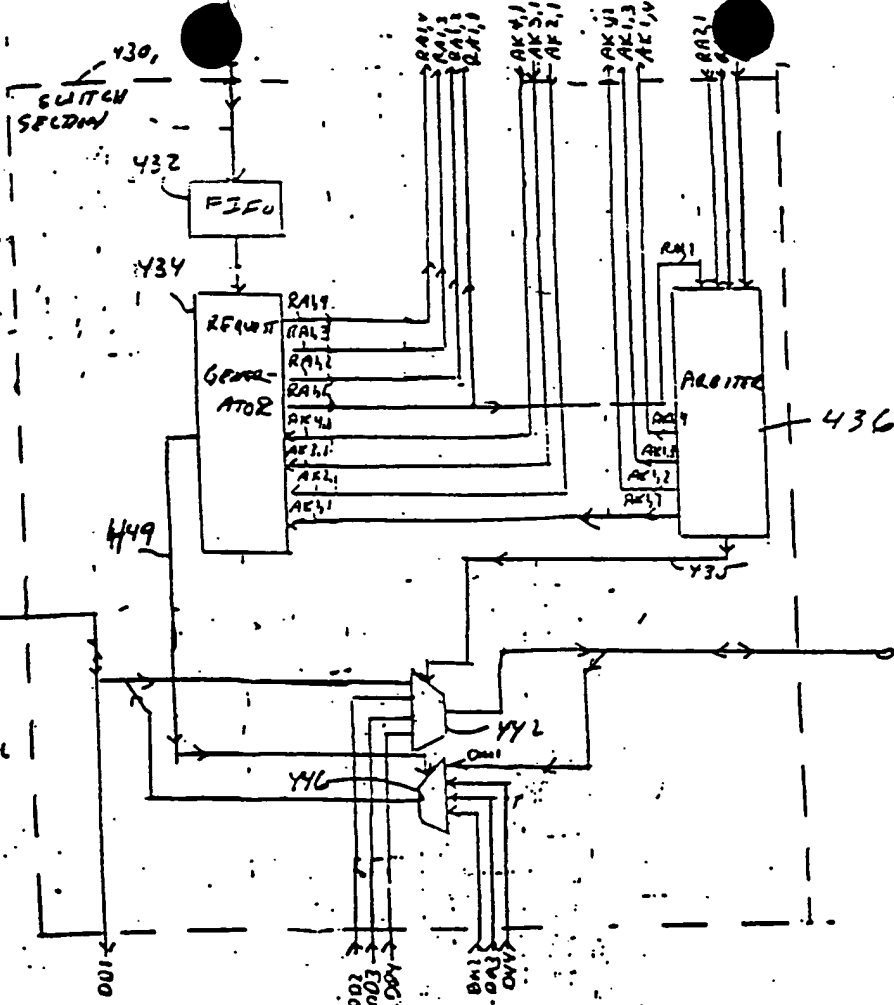


FIG. 8C

402A

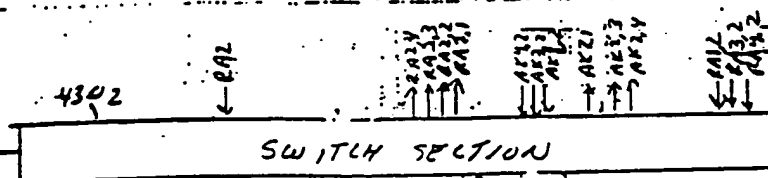
D1

TO/FROM
PORT
402A
DIRECTOR
1801



D2

TO/FROM
PORT
402A
DIRECTOR
1803



D3

TO/FROM
PORT
402A
DIRECTOR
1805



D4

TO/FROM
PORT
402A
DIRECTOR
1807

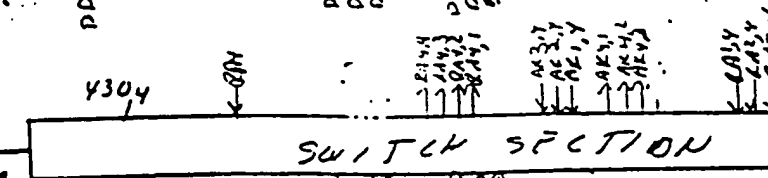
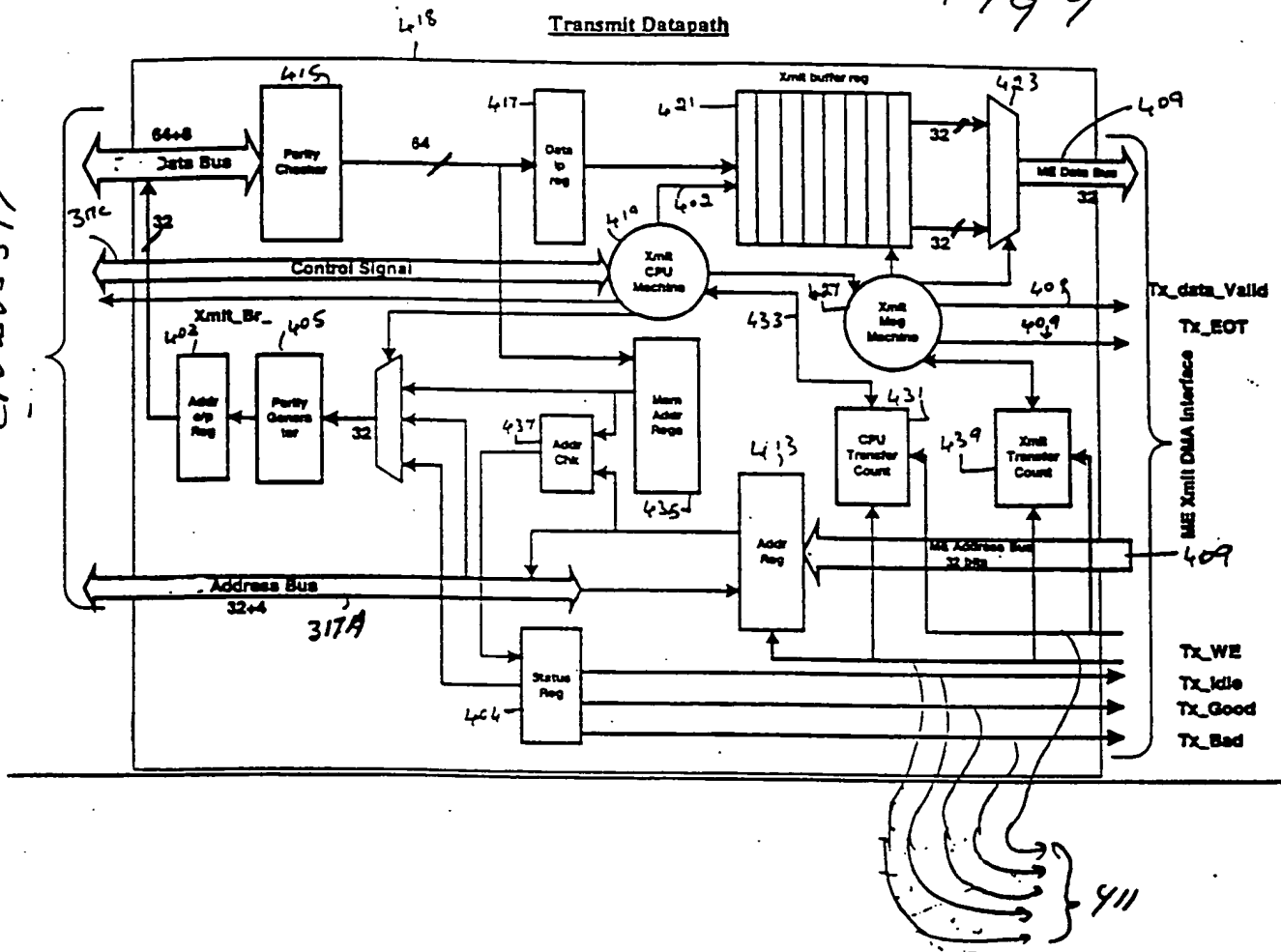


FIG 9

Transmit Datapath

CPU BUS 317



215 SNR BUS 317

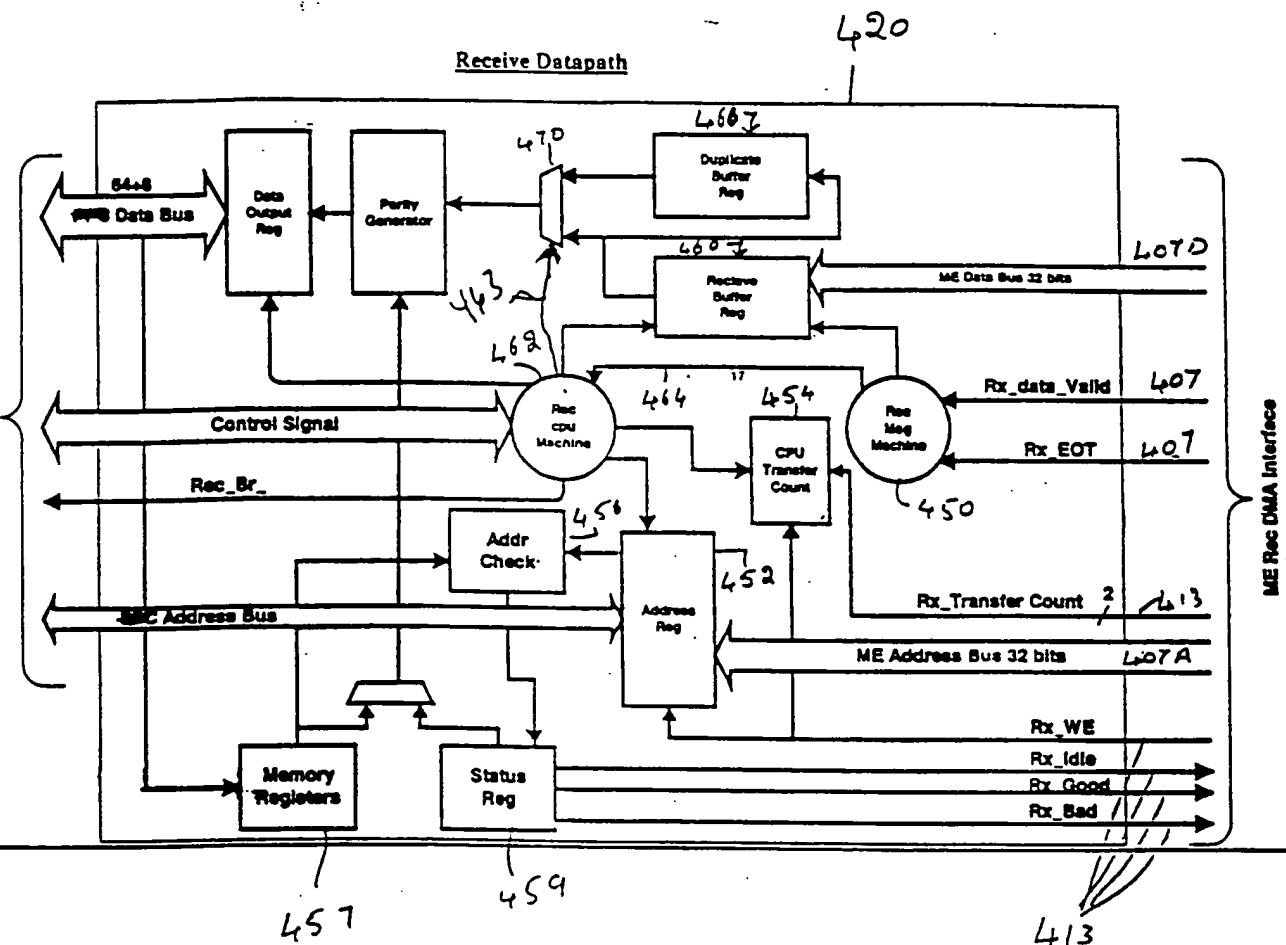
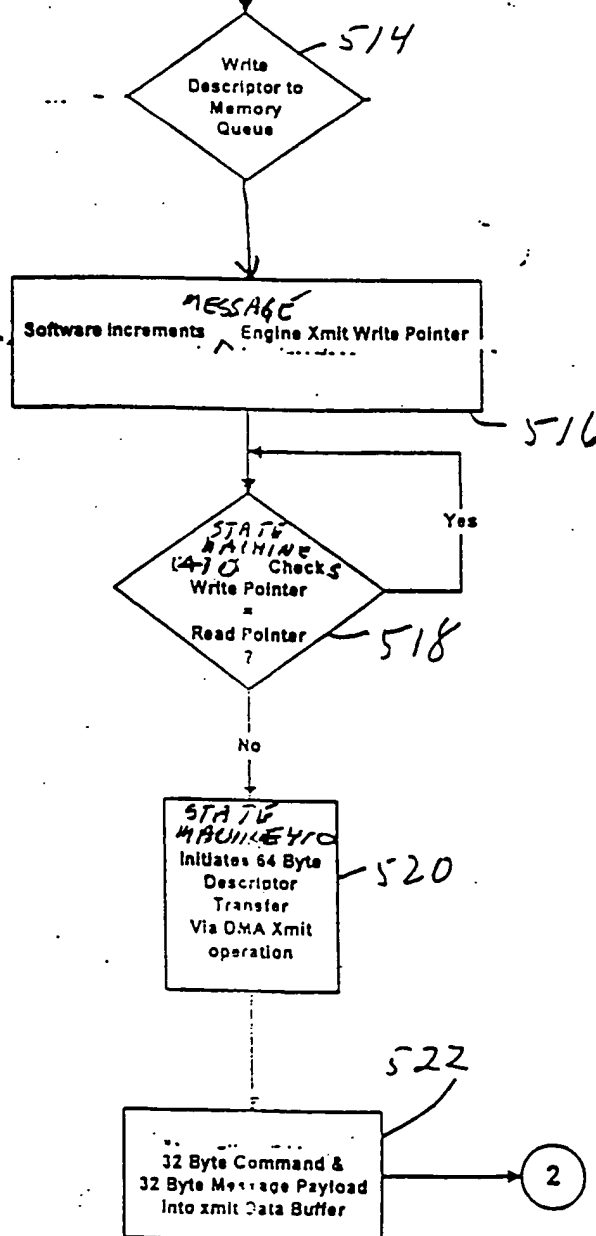
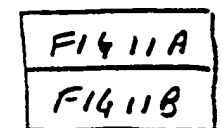
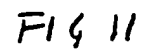
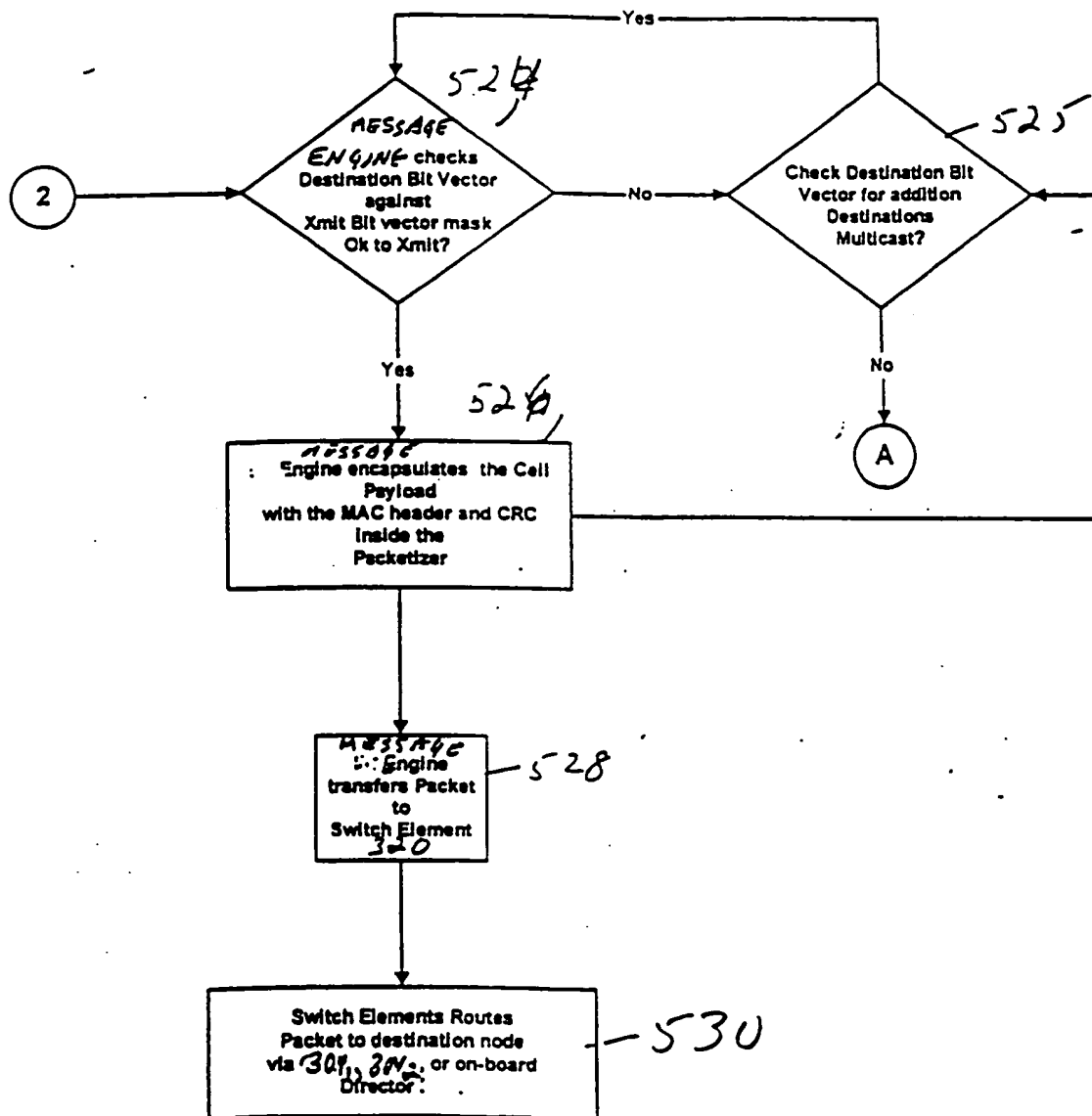


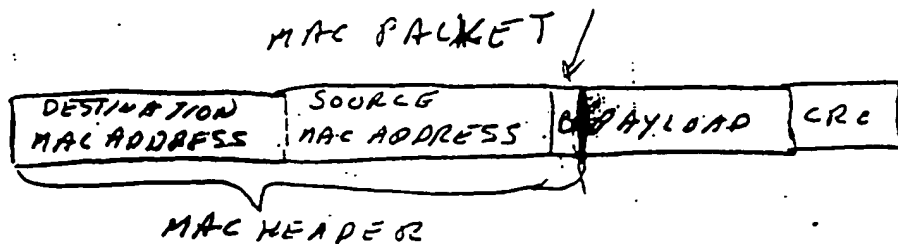
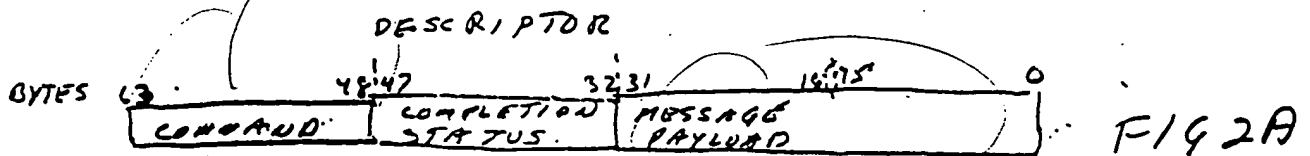
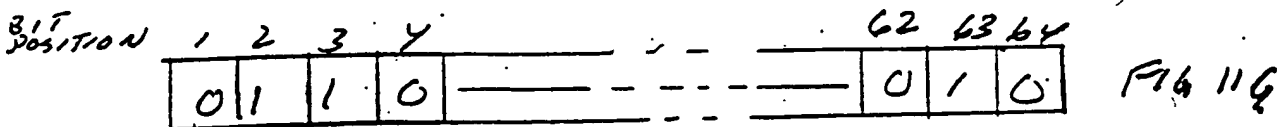
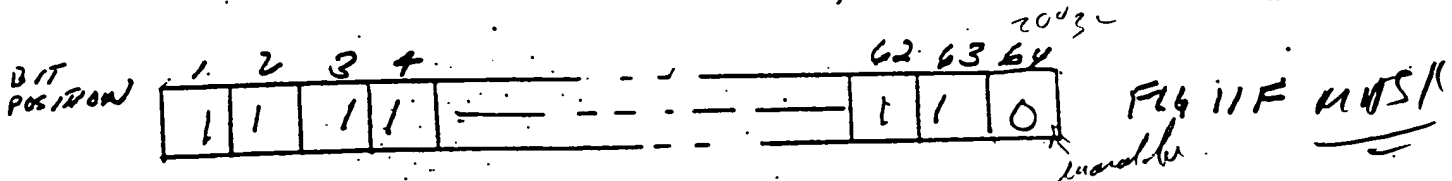
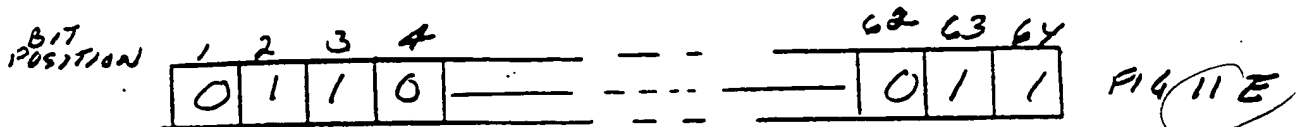
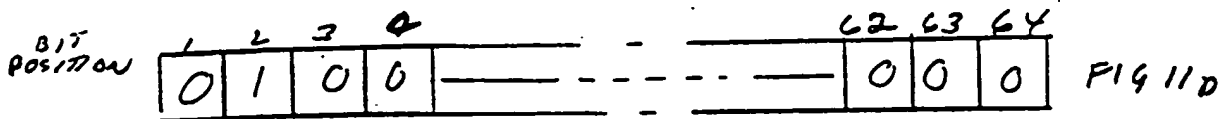
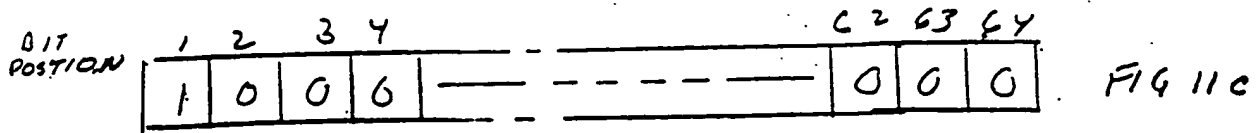
FIG. 11A



Message Bus Send Operation Continued

FIG 11B





Message Bus Receive Operation

FIG. 12A

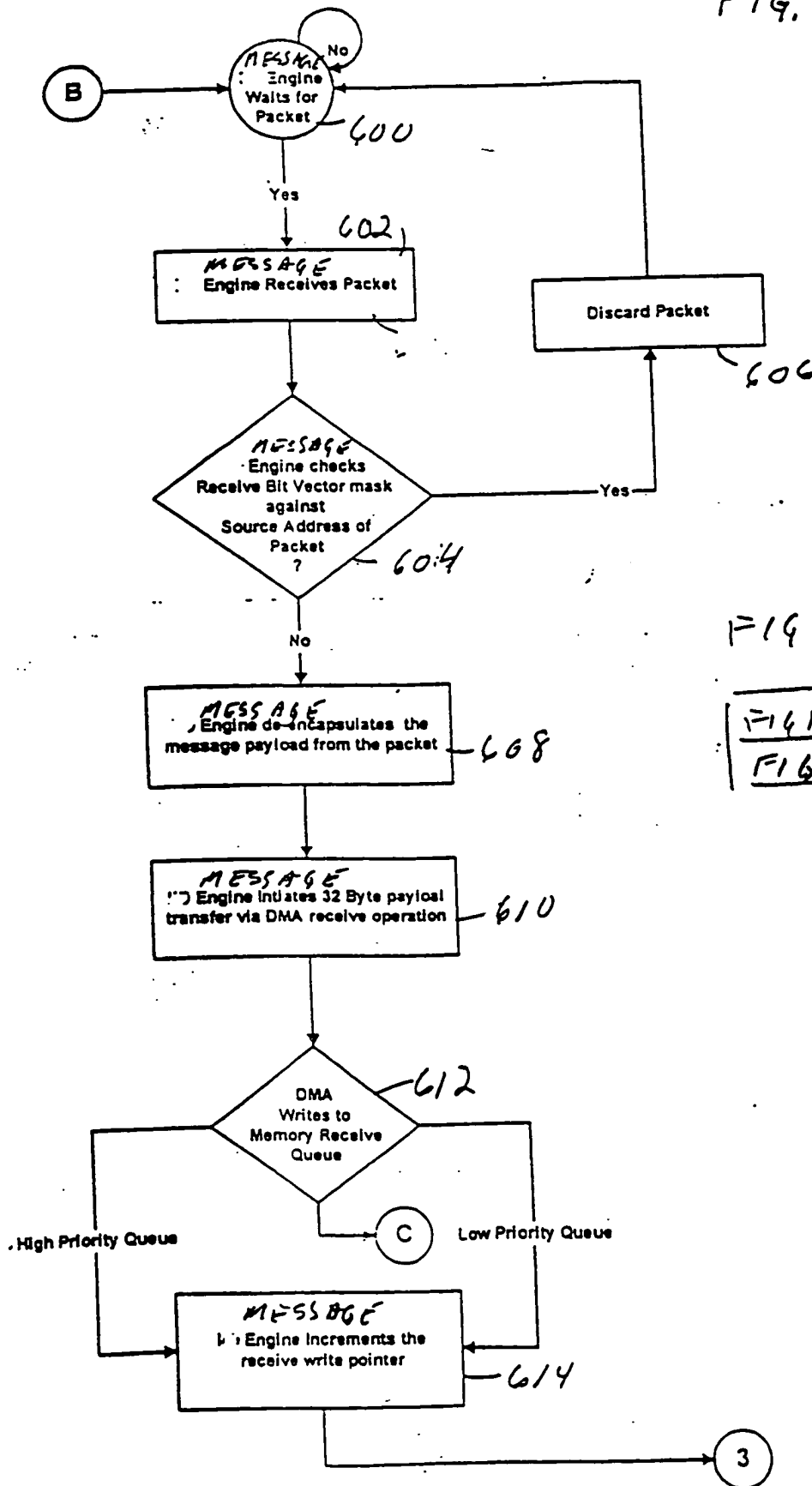
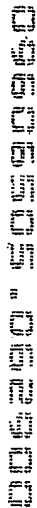


FIG 12

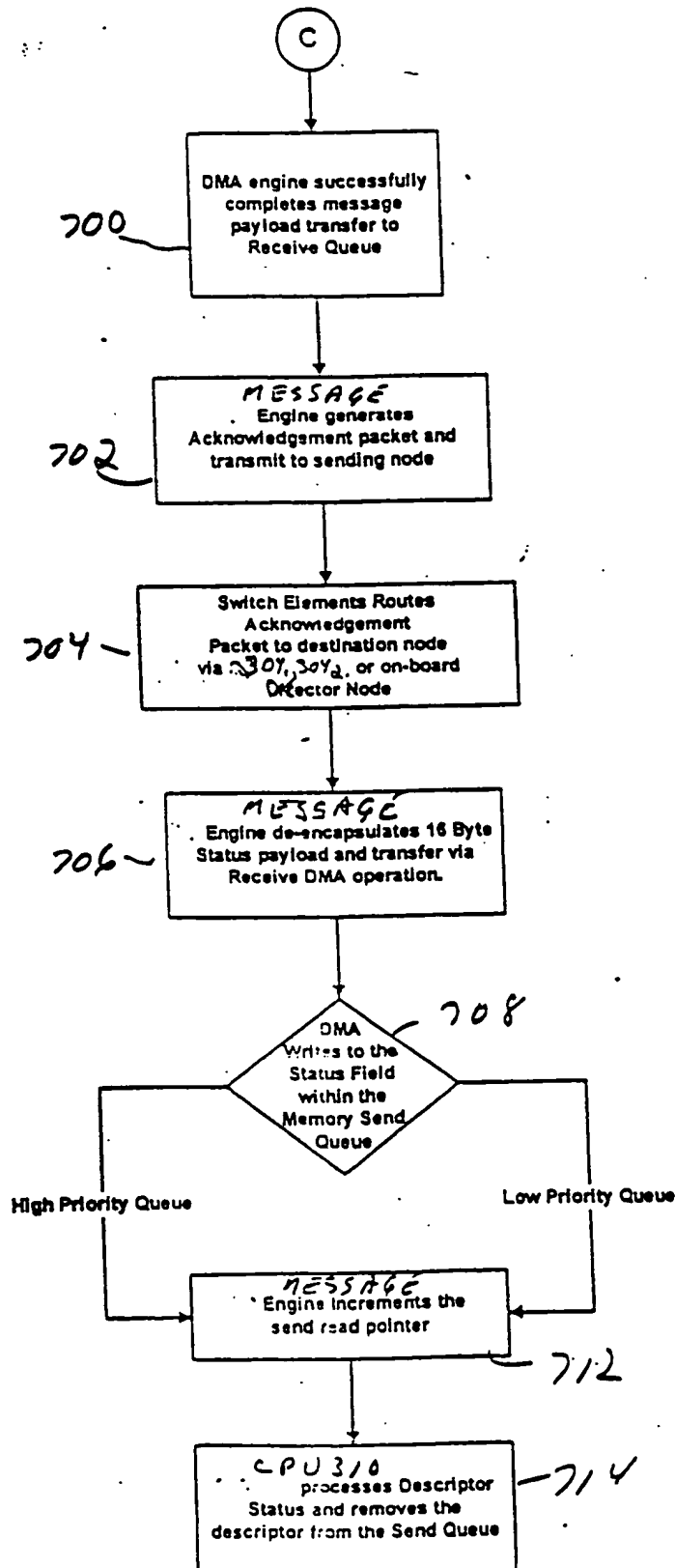
FIG 12A
FIG 12B

005290 5059960

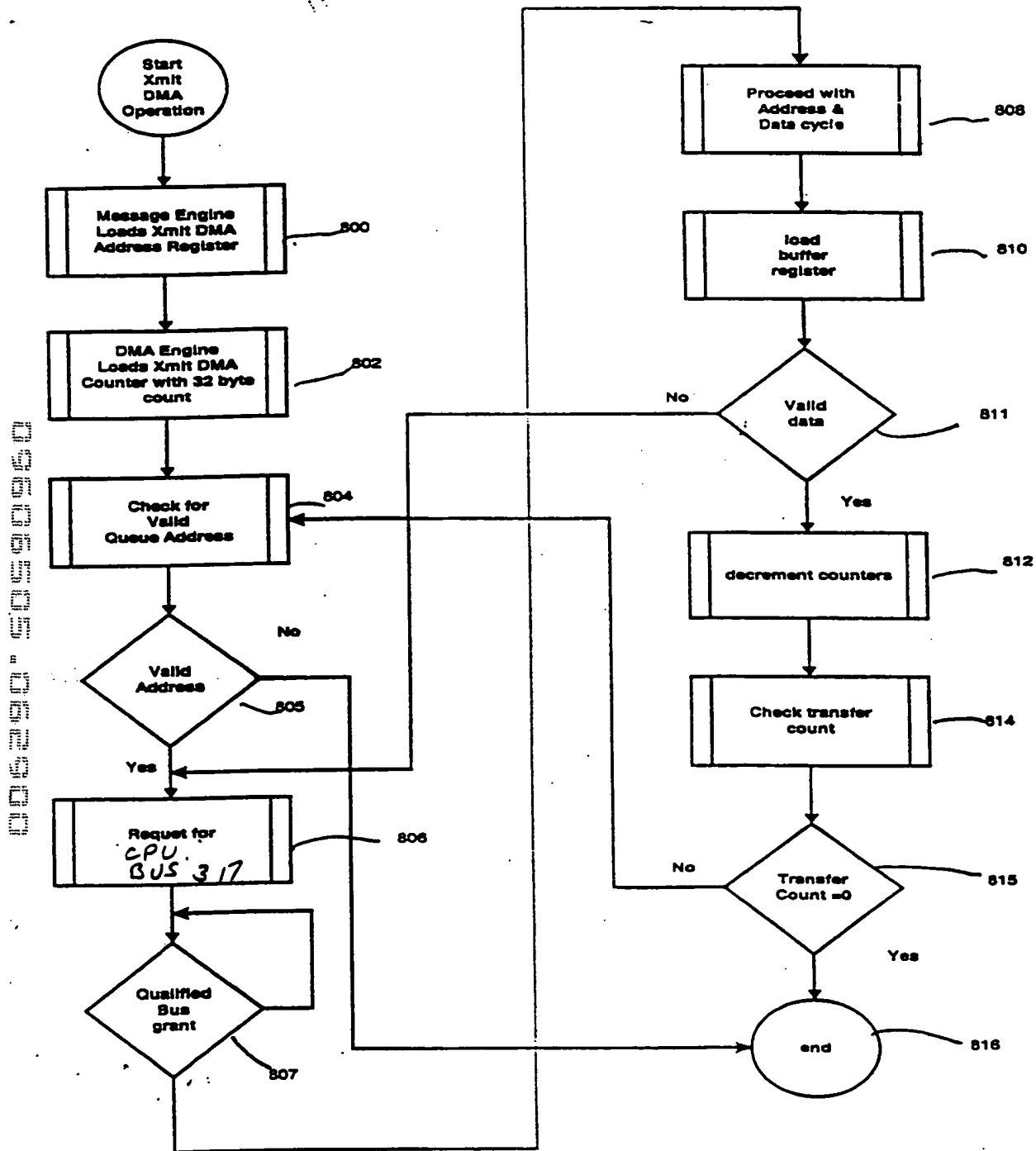
F1412B



Message Bus Acknowledgement Operation



Xmit CPU flow



F1414B

Xmit Msg flow

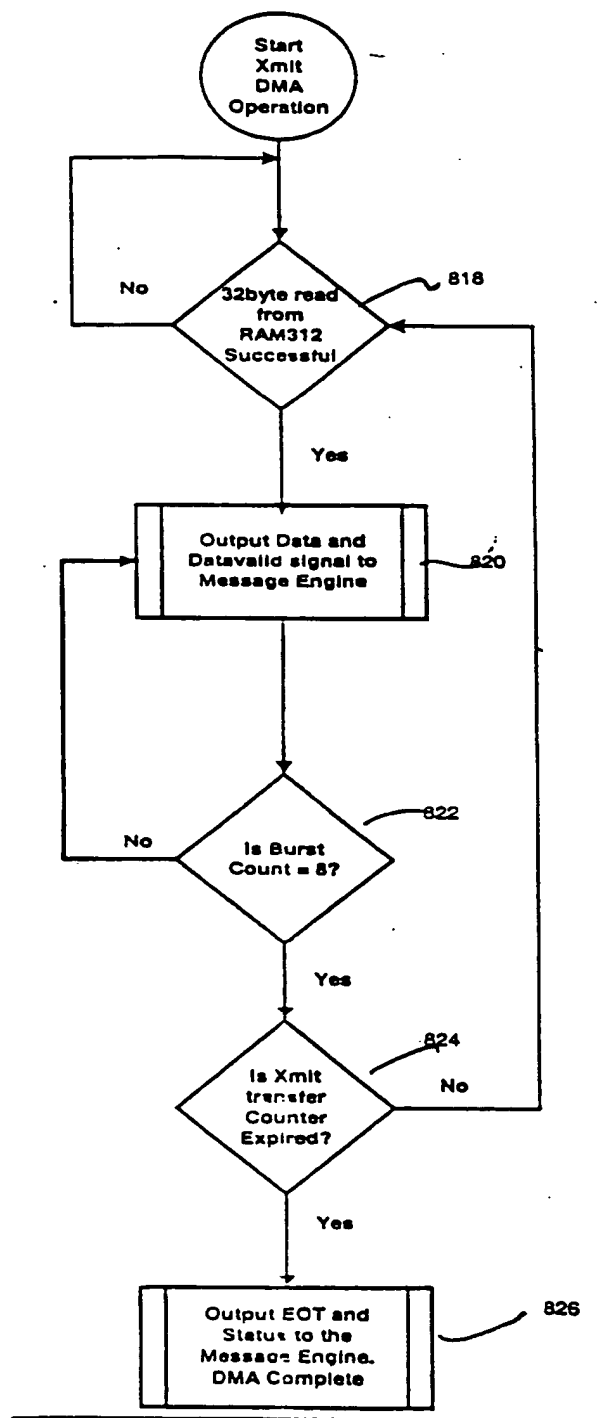
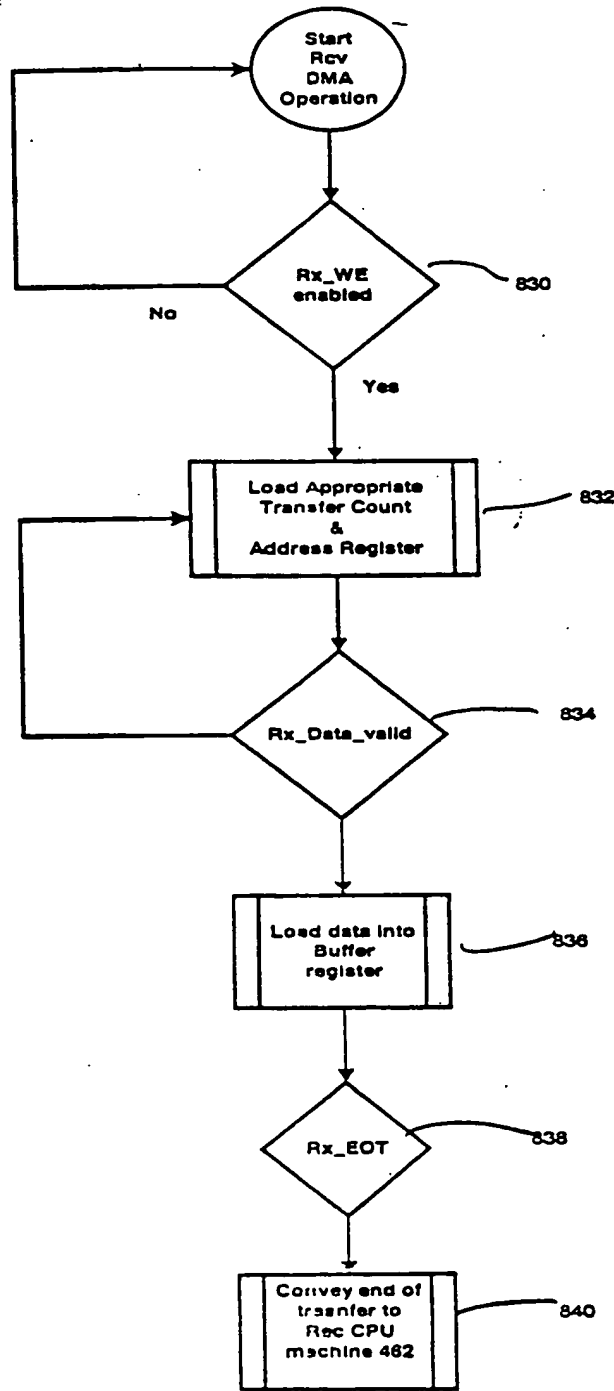


FIG 15A

Rec msg flow



005230" 60630950

F1415B

Rec cpu flow

